

IQAC EXTERNAL PEER TEAM REVIEW REPORT



Shri Vile Parle Kelavani Mandal's

DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING

(Autonomous College Affiliated to the University of Mumbai)

NAAC Accredited with "A" Grade (CGPA: 3.18)

Report on
the
Performance of the
Institution for the
Academic Year 2024-25
by

External Peer Team of IQAC
**IQAC-EXTERNAL PEER TEAM REPORT ON
PERFORMANCE OF THE COLLEGE FOR THE
ACADEMIC YEAR 2024-25**

A meeting to review the performance of the Internal Quality Assurance Cell (IQAC) of Dwarkadas J. Sanghvi college of Engineering was held on 17th December 2025 (Wednesday), 10 a.m. in the college premises. The external peer team comprised of following distinguished academicians:

Members of External Peer Team:

1. **Dr. Deven Shah** Director, Medical-Technology Academic Integration, Terna Trust
Principal – Terna Engineering College
Associate Dean – Faculty of Science and Technology, University of Mumbai
2. **Dr. G.T. Thampi** Principal, Thadomal Shahani Engineering college, Bandra, Mumbai
3. **Dr. Sudhakar Mande** Principal, Don Bosco Institute of Technology, Kurla, Mumbai

The Chairman of the IQAC, Dr. K.N. Vijaya Kumar along with the Principal, Dr. Hari Vasudevan welcomed the external members for the meeting. The committee members, after the brief initial introduction, verified the documents and evaluated the key parameters with respect to the overall performance of the college for the year. During this day-long review process, committee also took a round of the college premises accompanied by following internal members in order to perform evaluation.

IQAC Committee Members:

- Shri. Bharat M. Sanghvi – Vice President and Trustee, SVKM & Incharge, DJSCE
- Shri Sundeep Mehta – Nominee from Local Society
- Dr. Hari Vasudevan – Principal, Chairman
- Dr. K. N. Vijayakumar – Director, IQAC
- Dr. Trupti Paradkar –Deputy Director, IQAC
- Dr. Ramchandra Mangrulkar – Member
- Dr. Nilesh Patil –Member
- Dr. Kranti Ghag –Member
- Dr. Monika Mangla –Member
- Dr. Nilesh Marathe –Member

Dr. Deven Shah *LM* *LM*

- Dr. Ameya Kadam –Member
- Ms. Stevina Correia –Member
- Mr. Amit Choudhari –Member
- Mr. Greegory Mathew –Member
- Mr. Gopal Kritikumar Panchal –student (B.E. Mechanical)
- Mr. Mehul Mehta –Alumni (1994-98 batch)
- Mr. Dhruv Sampat – Alumni (2001-05 batch)
- Ms. Smit Shah – Alumni (2012-16 batch)
- Mr. Maulik Rajput – Alumni (2019-23 batch)
- Shri Gaurav Ghelani –Member, Industry: Recruiter
- Head of Departments – Permanent invites

Introduction and Background of the Institution

Shri Vile Parle Kelavani Mandal's (SVKM) Dwarkadas J. Sanghvi College of Engineering (DJSCE), established in 1994, has grown into a premier institution in engineering education over the past three decades, achieving substantial national and international recognition. The college has been recognized as an Autonomous Institution, with autonomy conferred by the University Grants Commission (UGC) for a ten-year period from the academic year 2019–20 to 2028–29. It is accredited by the National Assessment and Accreditation Council (NAAC) with a CGPA of 3.18 on a 4-point scale, corresponding to an "A" Grade and the same is currently valid.

Situated at a prime location in Mumbai, DJSCE features contemporary infrastructure, well-equipped laboratories, advanced computing facilities, and a comprehensive library. This robust academic environment is further strengthened by a team of highly qualified and experienced faculty members, alongside continuous upgrades to institutional resources. Consistent recognition by India Today and other reputed ranking agencies underscores its status as one of India's top private engineering institutions.

The Internal Quality Assurance Cell (IQAC), established in 2015, plays a central role in sustaining and enhancing the quality standards of the institution. Through continuous monitoring and regular quality enhancement initiatives, the IQAC contributes significantly to improving academic and administrative processes. The sustained efforts of the IQAC team

have been both noteworthy and effective in advancing institutional excellence.

Profile of the Institution

- **Name of the College** : Dwarkadas J. Sanghvi College of Engineering, No. U-15, J.V.P.D. Scheme, Bhaktivedanta Swami Rd, Opp.Cooper Hospital, Vile Parle, Mumbai, Maharashtra 400056
- **Name of the Principal** : Dr. Hari Vasudevan
- **Telephone/Fax /Email** : Phone No: 022 42335000
Mobile No: 9821464696
- **Email** : principal@djsce.ac.in, info@djsce.ac.in
- **Year of Establishment** : 1994
- **Whether private or government or University maintained** : Private and Self Financing
- **Autonomous Status** : UGC has conferred the Autonomous Status in the year 2019 for a period of 10 years from 2019-2020 to 2028-2029.
- **Name of the Affiliating University** : University of Mumbai, Fort, Mumbai.

For the academic year 2024–25, the institution offers a diverse portfolio of 8 Undergraduate, 3 Postgraduate, and 3 Ph.D. programs, all affiliated with the University of Mumbai. The college maintains an impressive track record of student achievements, with graduates securing placements in reputed multinational companies and gaining admission to leading global universities.

Further, the details regarding admitted students in the institute during the AY 2024-25 are as follows:



Programme Name	Programme Code	Seats Sanctioned	Students Admitted
Computer Engineering	60004	180	180+8*+1*
Electronics and Telecommunication Engineering	60002	180	180+4*+1*
Information Technology	60003	180	180+7*
Mechanical Engineering	60005	180	180+7*
Computer Science and Engineering (Data Science)	60009	180	180+9*+1*
Artificial Intelligence and Machine Learning	60017	120	120+6*+1*
Artificial Intelligence (AI) and Data Science	60018	60	60+3*+1*
Computer Science and Engineering (IOT and Cyber Security with Blok Chain Technology)	60019	60	60+3*+1*
Electronics and Telecommunication Engineering (PG) 60006	60006	9	1
Computer Engineering (PG)	60008	18	11
Mechanical Engineering (PG)	60007	18	2
Total			1207

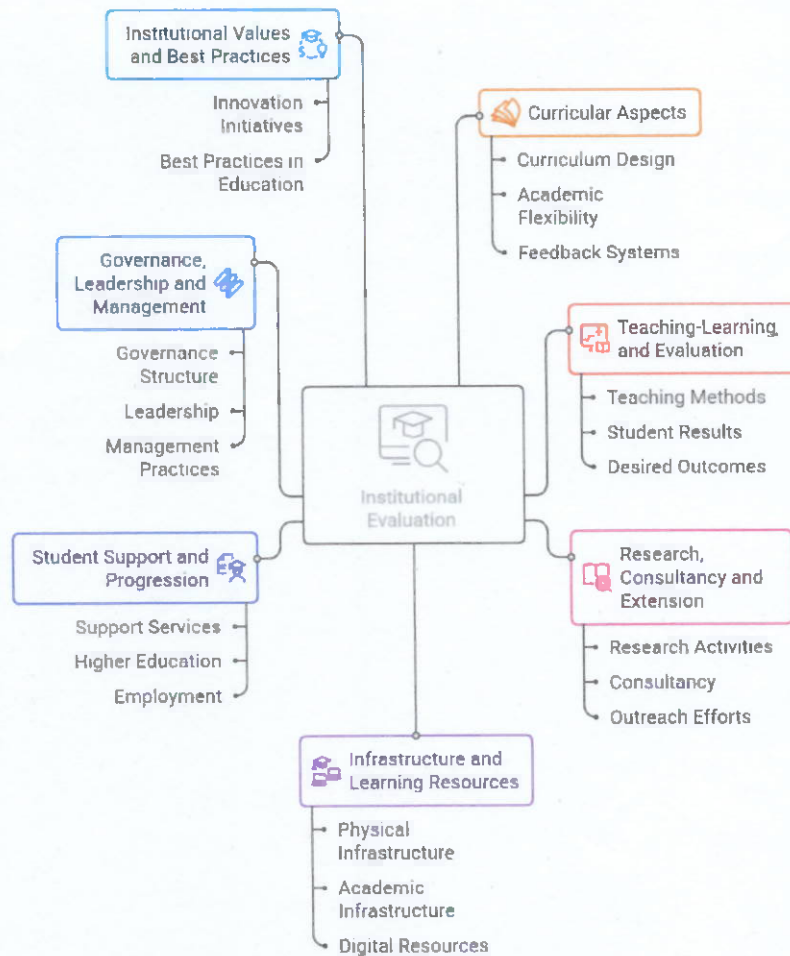
Here, the total count of 1207 admitted students refers to the sum of sanctioned intake plus the supernumerary seats (*) such as AICTE TFWS seats and J&K categories.

DJSCE focuses on Institutional Evaluation through different functional areas collectively contributing towards the overall effectiveness of an educational institution. The framework covers essential aspects such as Curricular Design, Teaching–Learning Processes, Research and Extension Activities, Governance and Leadership, Student Support Systems, Infrastructure, and Institutional Values. Each component represents a critical domain that influences institutional quality, performance, and continuous improvement. By examining these interconnected elements, institutions can identify strengths, address gaps, and implement

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strategies to enhance academic excellence and operational efficiency as follows:

Key Aspects of Institutional Evaluation



1. CURRICULAR ASPECTS

The institution's curricular framework is designed to ensure relevance, rigor, and alignment with the evolving needs of industry and academia. **Curriculum design** incorporates inputs from stakeholders such as faculty, industry experts, alumni, and employers to maintain outcome-based education and ensure that course content reflects current technological advancements. **Academic flexibility** is provided through elective courses, interdisciplinary offerings, value-added programs, and provisions for credit transfers, enabling students to tailor their learning pathways according to their interests and career goals. A robust **feedback system**—collecting inputs from students, faculty, alumni, and industry—ensures continuous curriculum review and timely updates, supporting sustained quality improvement and learner-centric education.

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1.1 Curriculum Design

DJSCE follows a well-structured and dynamic curriculum design process aimed at ensuring academic relevance, industry alignment, and holistic student development. The institution places strong emphasis on integrating current technological trends, emerging industry needs, and national educational reforms into its academic frameworks.

The curriculum is developed through a collaborative and consultative approach, involving key stakeholders such as alumni, industry employers, domain experts, and academicians. Their inputs—gathered through surveys, interactions, and structured feedback sessions—play a crucial role in refining course content, identifying skill gaps, and incorporating modern engineering practices.

In alignment with the National Education Policy (NEP) 2020, DJSCE promotes a multidisciplinary learning environment by offering Honours and Minors programs, enabling students to broaden their academic exposure and enhance their career prospects. The curriculum is designed to foster flexibility, innovation, and the development of both technical and non-technical competencies.

Once the draft curriculum is curated by the faculty and departmental committees, it is presented to the Board of Studies (BoS). The BoS members, comprising industry professionals and academic experts, provide constructive inputs that help refine the syllabus further. The revised curriculum is subsequently submitted to the Academic Council for approval, ensuring compliance with institutional policies, regulatory guidelines, and quality standards.

1.2 Academic Flexibility

DJSCE is committed to providing a learner-centric academic framework that promotes flexibility, choice, and holistic growth. The institution's academic structure is designed to accommodate diverse student interests and evolving industry requirements, empowering learners to customize their educational pathways.

To support this vision, DJSCE offers Honours and Minors programs aligned with the multidisciplinary approach encouraged by NEP 2020. These programs allow students to pursue advanced specialization or explore complementary domains beyond their core discipline, thereby broadening their academic and professional horizons.

The institute also provides flexibility through elective courses, including professional, open, and interdisciplinary electives that enable students to tailor their learning based on career aspirations and technological trends. The Choice-Based Credit System (CBCS) further enhances this adaptability, allowing students to manage their credit load and pace of learning effectively.

Additional provisions such as credit transfer, audit courses, and opportunities for industry-oriented certifications support students in pursuing varied academic interests while maintaining regulatory compliance. Flexible evaluation components, summer term courses, and provisions for undertaking projects or internships contribute to experiential and self-directed learning.

Through these mechanisms, DJSCE ensures that students receive broad-based, relevant, and adaptive education, fostering innovation, employability, and lifelong learning.

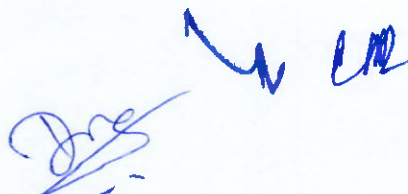
1.3 Feedback System

To ensure continuous improvement and relevance of its academic programs, DJSCE has established a structured and multi-layered feedback system integrated into the curriculum design process. Feedback is systematically collected from a diverse set of stakeholders, including students, alumni, employers, industry experts, faculty members, and academic peers.

Inputs are gathered through surveys, interactions, meetings, and review sessions, focusing on curriculum relevance, emerging technologies, industry expectations, and the effectiveness of teaching-learning components. Faculty analyze this feedback at departmental meetings, and the insights contribute directly to syllabus revision and enhancement.

The consolidated feedback is incorporated into the draft curriculum, before it is presented to the Board of Studies (BoS), where external experts provide further refinement. After BoS recommendations are addressed, the updated curriculum is submitted to the Academic Council for final approval.

This continuous and transparent feedback mechanism ensures that DJSCE's curriculum remains current, industry-aligned, learner-centric, and responsive to emerging academic and professional trends.



2. TEACHING-LEARNING AND EVALUATION

DJSCE adopts a student-centric and outcome-driven approach to teaching and learning. Faculty employ a blend of traditional lectures and modern pedagogies such as project-based learning, case studies, flipped classrooms, and industry-driven assignments. ICT-enabled classrooms, advanced laboratories, and the institutional Learning Management System enhance interactive and experiential learning. Highly qualified and experienced faculty further strengthen the quality of instruction and mentoring.

DJSCE also hosts vibrant professional student chapters, such as IEEE, IETE, CSI, ACM, SAE, ACMSIGH, NSDC, ISACA and S4DS, which conduct technical activities in embedded systems, computing, electronics, and software development. Chapters like, ISACA support learning in cybersecurity, auditing, and IT governance, while CODESTARS cultivates programming proficiency and computational thinking. These forums provide students with ample opportunities for workshops, competitions, research exposure, and industry interaction.

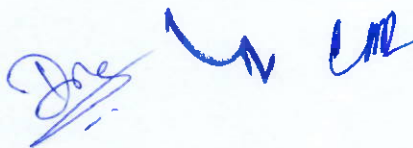
2.1 Teaching Methods

The institution ensures academic inclusivity through structured support for both slow learners and advanced learners. Slow learners benefit from bridge courses, remedial classes, and personalized mentoring, while advanced learners are offered opportunities such as research projects, advanced electives, certifications, and industry internships. Value-based courses including Universal Human Values, Business Communication and Ethics, and Professional Ethics promote holistic development, ethical responsibility, and strong communication skills.

2.2 Results and Progression

The evaluation process is transparent, efficient, and aligned with learning outcomes. Continuous internal assessments, project evaluations, lab assessments, and end-semester examinations ensure comprehensive evaluation. Systems, such as the Student Portal provide access to academic records, attendance, and results, while the Question Bank Management System (QBMS) standardizes exam paper preparation. The institution also utilizes an online paper-checking system, improving accuracy, speed, and transparency of evaluation.

Strong academic performance, consistent placement outcomes, and high progression to higher studies reflect the effectiveness of DJSCE's teaching-learning and evaluation framework,

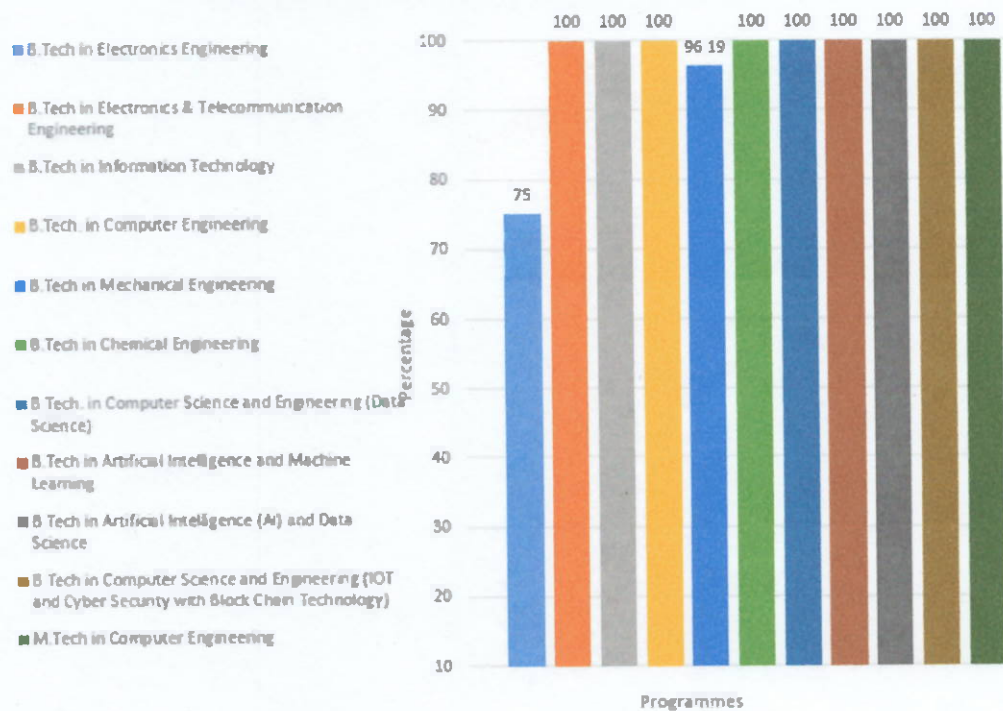


supported by robust digital systems, qualified faculty, and continuous quality enhancement practices.

2.3 Outcomes

The outcomes of the teaching–learning and evaluation processes at DJSCE are reflected in the consistently strong academic performance of its students. The structured pedagogy, supported by qualified faculty, modern laboratories, and ICT-enabled teaching methods, results in high pass percentages, excellent semester-wise performance, and steady improvement in course outcomes and program outcomes. Continuous internal assessments, project-based evaluations, and transparent examination systems ensure that students demonstrate both conceptual clarity and practical competency. The results of DJSCE are as follows:

Convocated Students (In Percentage) AY 2024-2025



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3. RESEARCH, CONSULTANCY AND EXTENSION

The institute fosters a strong culture of innovation and knowledge transfer through its IPR and Entrepreneurship Cell, supported by multiple Ph.D. centers. Its robust research ecosystem comprises **3 research centers**. Since 2015, the institute has been offering University of Mumbai–approved Ph.D. programs in **Computer Engineering, Electronics and Telecommunication Engineering, and Mechanical Engineering**.

The department-wise count of faculty members publishing research papers in journals and book chapters is as follows:

Department	Journals	Book Chapters
Computer Engineering	26	14
Electronics and Telecommunication Engineering	25	6
Information Technology	7	25
Mechanical Engineering	21	8
Computer Science and Engineering (Data Science)	19	18
Artificial Intelligence and Machine Learning	3	3
Artificial Intelligence (AI) and Data Science	4	4
Computer Science and Engineering (IOT and Cyber Security with Blok Chain Technology)	11	15
Humanities	8	

Faculty members at DJSCCE has been actively involved in various activities and has received awards and recognitions as follows:

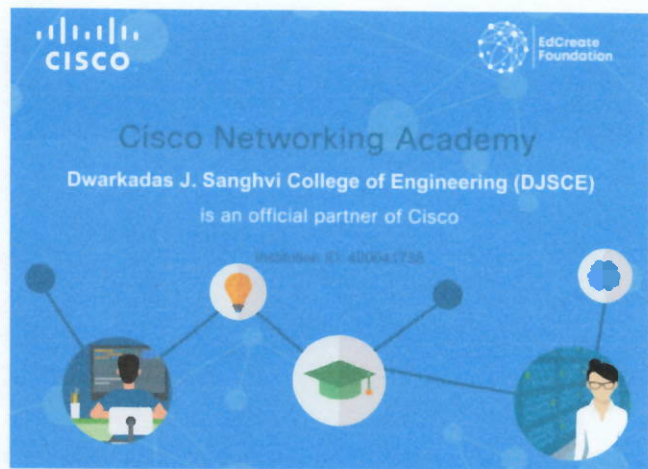
Details of the Award/Recognition	Count
Prestigious Education Excellence Award 2025 received by Faculty Members	10
NPTEL	43
Copyright and Patents	31
Certificate of Merit for research project presentation	2

Meritorious Student Certificate	1
Research Grant	4

DJSCE has secured research funding of ₹84.37 lakh as follows:

Project Name	 Mozilla Foundation Grant	 Google Explore CSR
Amount (Lakhs)	20.75257	63.61875
Duration	1 year 3 months	1 year
Funding Agency	Mozilla Foundation	Google
Type	Non Government	Non Government

In order to escalate the technical competence of faculty members and students, DJSCE has signed 3-Years MoU with CISCO and Eduskills Foundation.



DJS *CM* *CM*

4. INFRASTRUCTURE AND LEARNING RESOURCES

State-of-the-Art Infrastructure

The institution features 44 ICT-enabled, air-conditioned classrooms with smartboards, 55 modern laboratories, 1316 desktops, 10 tutorial rooms and well-stocked central and departmental libraries. A high-speed Wi-Fi campus supports seamless connectivity, while conference and seminar halls host academic events. Dedicated labs are available for 8 UG, 3 PG, and 3 Ph.D. programs.

Holistic Student Development

Students thrive in a vibrant campus life through cultural committees, hobby clubs, and annual fests, judged by faculty. Facilities include a gymnasium with professional trainers. Outdoor sports like football, volleyball, and cricket are organized on nearby grounds. Annual inter- and intra-college competitions, along with access to the SVKM playground, enrich student engagement.

The college library is fully Wi-Fi enabled and accessible 24/7, facilitating a smooth transition from traditional to digital learning environments. The college library is fully supported by the KOHA Integrated Library Management System (v23.05.02).

5. STUDENT SUPPORT AND PROGRESSION

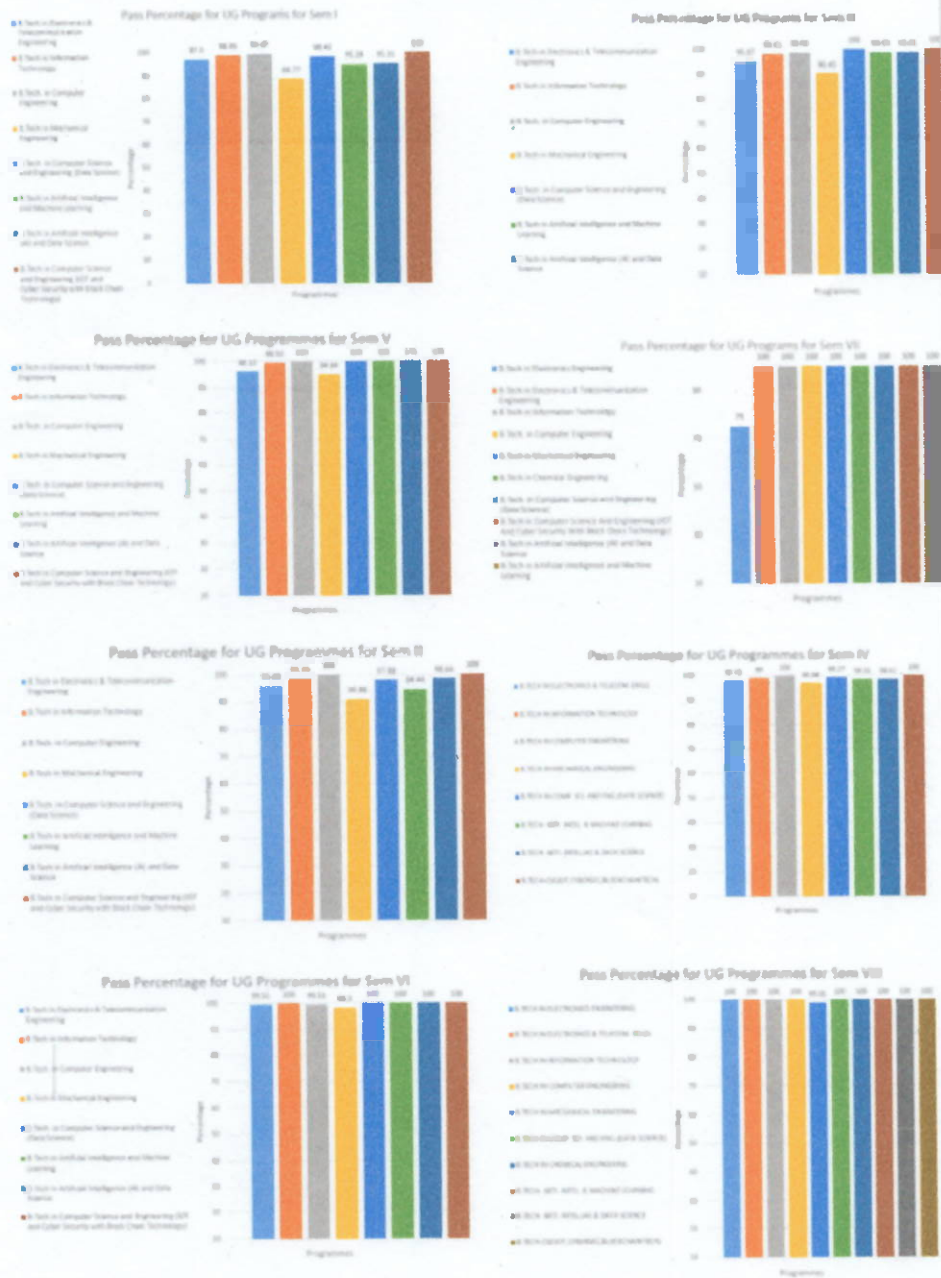
5.1- Student Progression

DJSCE systematically monitors students' academic performance and overall development to ensure steady progress throughout their program. Continuous assessments, mentoring, and remedial support help students strengthen their competencies and achieve learning outcomes. The following section presents semester-wise results that reflect this ongoing academic advancement.

Dr. Dinesh Chandra *MS* *CM*

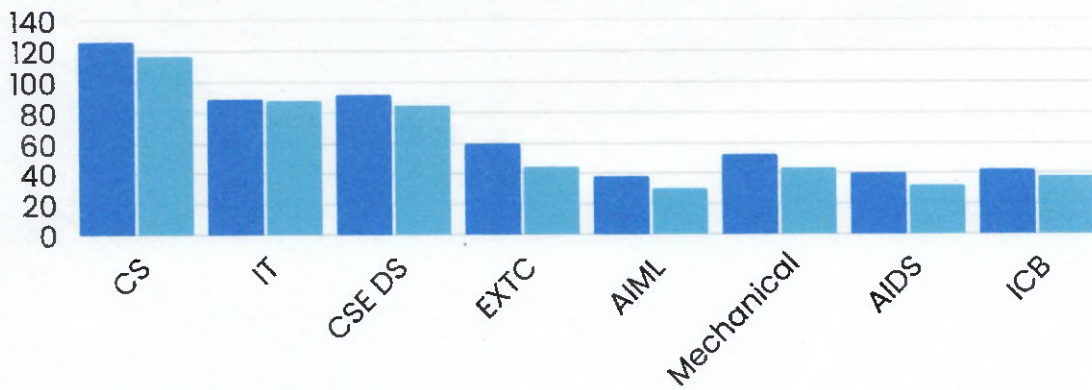
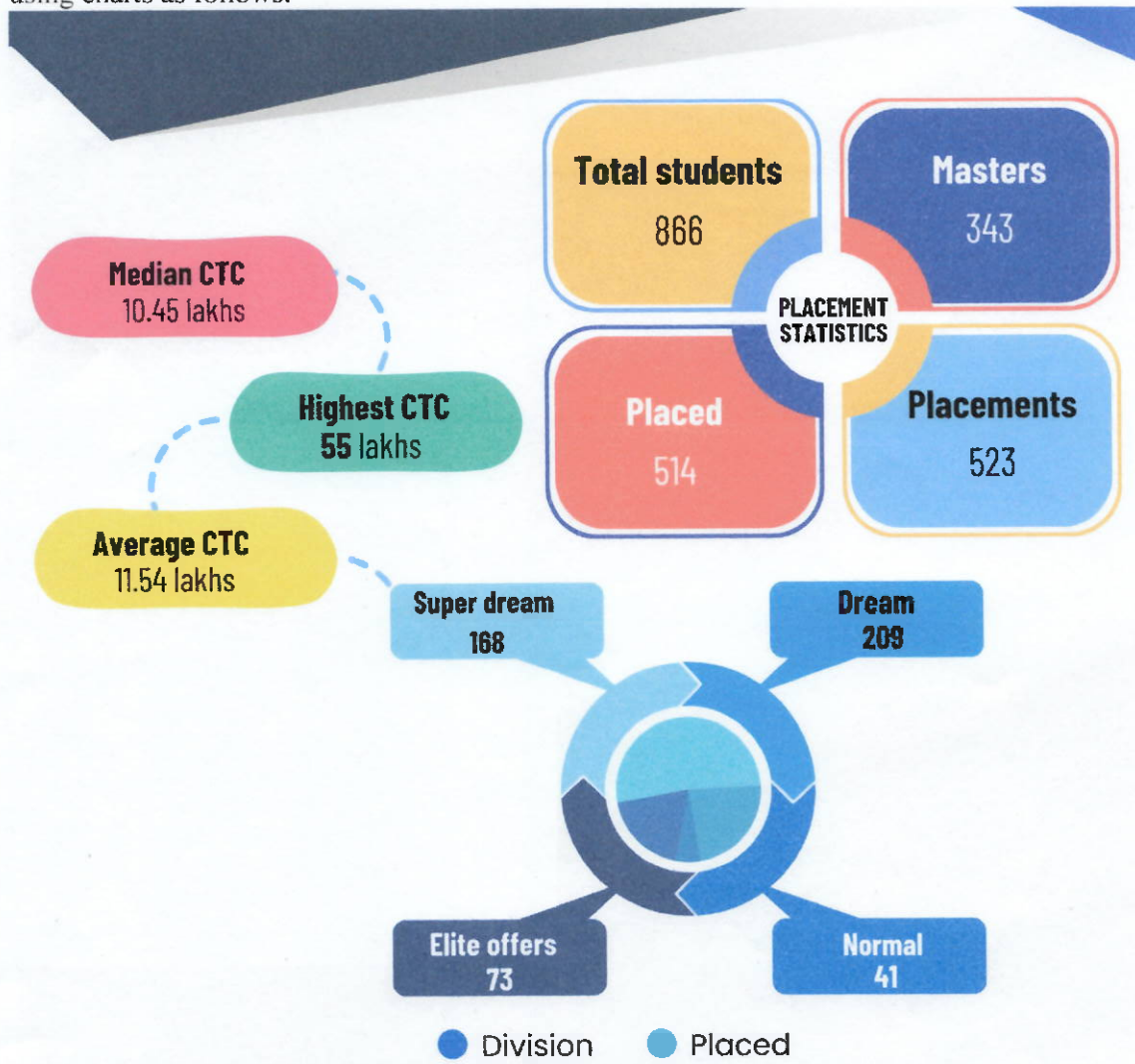
The semester-wise result for students of DJSCE during 2024-25 is illustrated as follows:

Pass Percentage



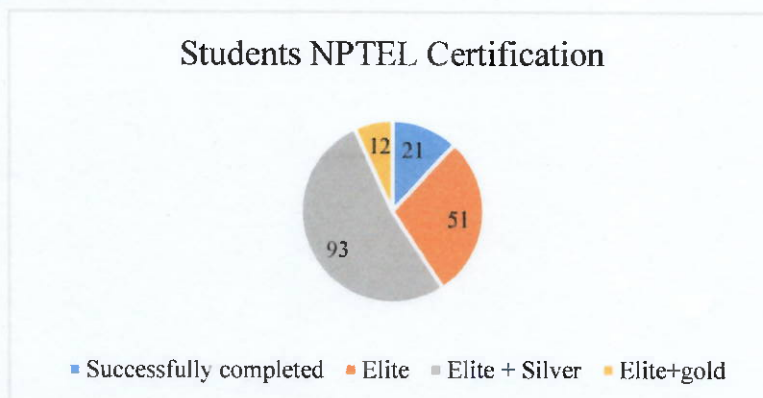
Dr. [Signature]

The placement and higher studies for the students of DJSCE during 2024-25 are illustrated using charts as follows:



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Additionally, students are consistently encouraged to pursue NPTEL certifications. The following is a snapshot of the data for AY 2024–25:



DJSCE has distinguished itself as a leader in prestigious competitions, including the Smart India Hackathon. Students are also actively involved in various technical activities and consistently win accolades, reflecting their strong technical aptitude and innovation mindset.

Name of Department	Accolades won in Technical Activities
Computer Engineering	09
Electronics and Telecommunication Engineering	03
Information Technology	28
Mechanical Engineering	09
Computer Science and Engineering (Data Science)	28
Artificial Intelligence & Data Science	19
Artificial Intelligence & Machine Learning	22
Computer Science and Engineering (ICB)	16

Internship Summary Report for AY 2024-25

During the academic year 2024-25, a total of 579 students actively participated in internships, significantly contributing to their academic and professional growth. The internship program, spanning from July 2024 to June 2025 providing students with a diverse range of experiences.

Department-Wise Distribution

Name of the Department	Number of students did the internship
AI_DS	56
COMP	104
CSE (ICB)	33
CSE (DS)	127
IT	102
MECH	26
AI_ML	112
EXTC	19
Total	579

The internship records encompass both paid and unpaid opportunities, emphasizing the commitment to offering students a variety of experiences. This approach recognizes the value of internships in skill development, irrespective of financial compensation.

5.2 - Student Participation and Activities:

D.J. Sanghvi fosters a holistic environment for students through year-round extra-curricular and co-curricular activities. The annual festival "Trinity," organized by the Student Council, features cultural, technical, sports, and literary events, including highlights like DJ Night and Musical Concerts. Professional student chapters, such as ACM, CSI, SAE, IEEE and IETE host technical events like seminars, competitions, and industrial visits. Departments have advisory boards comprising industry experts, faculty, alumni, and students, while class representatives and maintenance committees address academic and facility-related grievances. The NSS unit actively contributes to societal welfare, and the Alumni Association strengthens ties between alumni and the institute, enhancing its reputation through collaborative efforts.

To participate in SIH, 50 teams were nominated from the college through Internal Hackathon conducted in the month of September 2024 and were registered in the SIH portal by College SPOC, Dr. Vinaya Sawant (Head, Department of IT). The teams selected through Internal Hackathon submitted their problem solution for their respective problem statements in the form of PPTs in SIH portal. The submitted solutions were scanned and judged rigorously by SIH judges at National level. SIH has allowed maximum 500 solutions from various colleges across India for each problem statements they released. SIH team then selected 5-7 colleges for each



problem statement for Grand Finale scheduled from 11th to 15th December. 12 teams from D. J. Sanghvi College of Engineering were selected by SIH panellists for Grand Finale for different problem statements at different nodal centres across India for an offline Hackathon in Software category and 2 teams were selected in Hardware category. We are proud to announce that 5 teams (software category) and 1 team (hardware category) from D. J. Sanghvi College of Engineering were declared winners across various nodal centres in India.



SIH GRAND FINALE - FISRT PRIZE WINNER (Rs. One Lakh)

Achievements at a Glance

Team	SIH Kottam	
Competition	AIJAY INDIAN 2023	
Date	17th March to 20th March 2023	
Location	Hopital, India	
Achievements	<ul style="list-style-type: none"> 1st in Cost 1st in Design 1st in Business Presentation 	

Team	SIH Kottam	
Competition	Go-Kart Design Challenge 2023	
Date	17th Feb to 27th Feb 2023	
Location	Kart Motor Speedway in Coimbatore, India	
Achievements	<ul style="list-style-type: none"> Audience Award: 3rd Skidpad Round: 3rd Design Presentation: 4th Cost Budget presentation: 4th Appearance Post: 5th Business Plan presentation: 6th Endurance race: 7th 	

Team	SIH Phoenix	
Competition	SIH PHOENIX 2023	
Date	23rd March 2023	
Location	In Asha Padigar	
Achievements	<ul style="list-style-type: none"> Overall 2nd place 	

Team	SIH Noida	
Competition	SIH Auto Design West 2024	
Date	4th to 27th April 2023	
Location	California, USA	
Achievements	<ul style="list-style-type: none"> Best Cost Design Report Worthbody: 4th Cost Presentation Worthbody: 2nd Overall Worthbody: 2nd 	

Team	SIH Delhi	
Competition	Design Auto Vehicle Championship 2023	
Date	17th March to 27th March 2023	
Location	India, India	
Achievements	<ul style="list-style-type: none"> Design Report: 4th rank position Manufactured: 4th rank Plan and Cost report: 4th rank Overall rank: 4th rank Win the Go Green Energy award 	

Team	SIH Bhopal	
Competition	Go Kart Design Challenge 2023	
Date	17th to 27th January 2023	
Location	Kart motor speedway, Coimbatore, Tamil Nadu, India	
Achievements	<ul style="list-style-type: none"> 4th Overall 4th in Cost 4th in Design Feature 4th in Business Plan 	

Team	SIH Jamshedpur	
Competition	SIH Auto Design West 2024	
Date	4th to 27th April 2023	
Location	California, USA	
Achievements	<ul style="list-style-type: none"> Project NIMBUS: First High Powered Rocket of D J Sanghvi College with an Apogee of 1 Km to be launched on July 2023 	

Team	SIH Noida	
Competition	SIH Auto Design West 2024	
Date	3rd October 2023	
Location	Kart motor speedway, Coimbatore, Tamil Nadu, India	
Achievements	<ul style="list-style-type: none"> AIIR 1 - Acoustics and Structure AIIR 2 - Endurance and Cost Evaluation AIIR 3 - Design Evaluation AIIR 4 - Overall in EV Category 	

Team	SIH Auto	
Competition	SIH Auto Design West 2024	
Date	4th to 27th April 2023	
Location	Spain, India	
Achievements	<ul style="list-style-type: none"> 5th Position Overall 	

Team	SIH Milan	
Competition	Shell Eco-Marathon 2024 (Indonesia, Asia Pacific, and Middle East Region)	
Date	27th to 31st July 2024	
Location	Pertamina Mandala International Street Circuit in Lombok, Indonesia	
Achievements	<ul style="list-style-type: none"> Represented India in the opening ceremony of SEM 2024 among other 9 Indian teams. All-2 history shovels prototype category 13th overall position globally 	

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Team's Achievement 2024

- **International Rover Challenge 2024**
3rd Position Worldwide
- **International Space Drone Challenge 2024**
2nd Position Worldwide
- **European Rover Challenge 2024**
11th Position Worldwide



Team's Achievement 2025

- **International Rover Challenge 2025**
3rd Position Worldwide
- **International Space Drone Challenge 2025**
2nd Position Worldwide
- **European Rover Challenge 2025**
11th Position Worldwide

Featured in the Corporate Brief Section of the Indian Express and The Financial Express

UPDATES @ J. SANGHVI COLLEGE
President J. Sanghvi College of Engineering, Mumbai among the **100 Most Innovative** in the world in the **2024** edition of the **Global Innovation 1000** award. The award is given to the most innovative companies in the world. The award is given to the most innovative companies in the world. The award is given to the most innovative companies in the world.

1275 **Kinross**

1275 **Skylark**

1275 **Karling**

THE WEEK HANSA RESEARCH SURVEY 2024
DJCE RANKED as

1st

Average Top Private Engg. Colleges (West Zone)

1st

Average Top Private Engg. Colleges (Maharashtra)

1st

Average Top Private Engg. Colleges (Mumbai)

11th

Average Top 246 Private Engg. Colleges (All India)

35th

Average Top 275 Engg. Colleges (All India)



RECENT BANKINGS OF J. SANGHVI COLLEGE OF ENGINEERING, DJCE (JULY 2024-25)

Rankings of DJCE in the West Zone of the country (2024-25)

- "Best Private Engineering College" (Rank 12) in the West Zone - India Today (June 07, July 2024)
- "1st Best Private Engineering College" (Rank 1) in the West Zone & 1st among the Top Engineering Colleges of Mumbai - Power of India (June 07, July 2024 & 07, June 2024)
- "1st in the West Zone of the country & 1st among the Top Private Engineering Colleges of Maharashtra as well as 1st among the Private Engineering Colleges of Mumbai - The South Asian (07, June 2024)
- "1st in Maharashtra from - Competition Success Stories (07, June 2024)

Overall Ranking of DJCE in the West Zone of the country (2024-25)

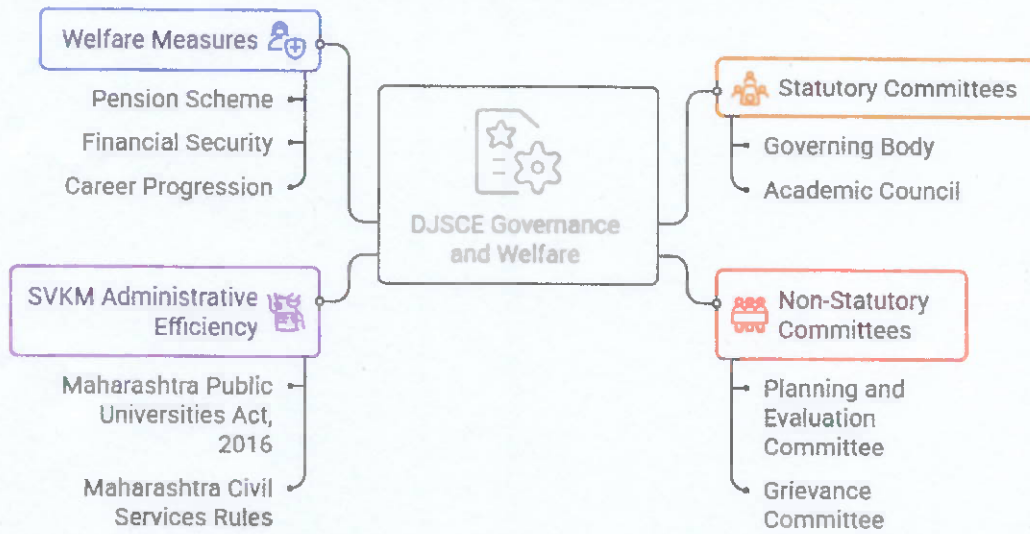
- 1st among the Top 10 Private Engineering Colleges of the country - India Today (June 07, July 2024)
- 1st among the Top 10 Private Engineering Colleges of the country & 1st among the Top 10 Private Engineering Colleges of the country - India Today (June 07, July 2024)
- 1st among the Top 10 Private Engineering Colleges of the country & 1st among the Top 10 Private Engineering Colleges of the country - The South Asian (07, June 2024)
- 1st in the Top Engineering Colleges of the West Zone of India & 1st in the West Zone of India - Competition Success Stories (07, June 2024)
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- 1st among the Top 10 Private Engineering Colleges of the country & 1st among the Top 10 Private Engineering Colleges of the country - The South Asian (07, June 2024)

6. GOVERNANCE, LEADERSHIP AND MANAGEMENT

DJSCE demonstrates strong, inclusive governance through a network of statutory and non-statutory committees, engaging students, faculty, and staff in decision-making across academics, welfare, cultural, and sports activities. Guided by a clear Institutional Development Plan (IDP), the college aligns its vision, mission, and strategic priorities with actionable initiatives, promoting sustainability, innovation, and academic excellence.

Supported by SVKM, DJSCE maintains administrative efficiency, regulatory compliance, and transparency in appointments. The institution also prioritizes faculty and staff welfare through career support, financial security, and a pension scheme that extends benefits to employees and their spouses, reflecting a commitment to compassionate and sustainable employee care.

DJSCE has various statutory and non-statutory committees as depicted below:



DJSCE also paid out its former faculty and staff members during academic year 2024-25 as follows:

1. Faculty and staff members availed SVKM's pension pay-outs.

Sr. No.	Name of Staff	Pension p.m.
1	Narsimhan Chari	15000/-
2	Ramesh Vulavala	15000/-
3	Anusuya Datta	15000/-
4	Ashok Waghela	5000/-
5	Mangal Waghela	2500/-
6	Alpana Mahapatra	15000/-
7	Usha Vishwanathan	15000/-
8	Mr. Radhakrishnan P	5000/-
9	Mr. Vijay Chavan	5000/-

2. Gratuity paid in 2024-25

Sr. No.	Name of Staff	Gratuity
1	Mrs. Lakshmi Kurup	5,34,300/-
2	Ms. Mitchel D'Silva	3,00,600/-
3	Mr. P.R. Radhakrishnan	5,41,500/-
4	Mr. Vijay Chavan	5,95,350/-
5	Mrs. Sangeeta Pawar	3,97,100/-
6	Mr. Prajwal Deshmukh	49,750/-

3. Leave Encashment

Sr.No.	Name of Staff	Amount
1	Mr. P.R. Radhakrishnan	3,61,000/-

(Handwritten signatures)

4. Faculty and staff members availed Leave Travel Concession

Sr.No.	Name of Employee	Amount Paid
1	Mr. Deepak Pandurang Patil	16000/-
2	Mr. Birendra R. Singh	9570/-
3	Mrs. Rekha Suresh Pachupate	2406/-
4	Mr. A. B. Tambe	2196/-
5	Mr. RAKESH SINGH	7887/-
6	Mr. Vinayak Nayak	7887/-
7	Mrs. Ankita Pednekar	3690/-
8	Ms. Rashmi Ravikumar	6456/-
9	Mr. Prakash. S. Bavdane	10516/-
10	Mr. Kanubhai Waghela	7887/-
11	Mr. Arvind Singh	5802/-

7. INSTITUTIONAL VALUES AND BEST PRACTICES

DJSCE upholds values of integrity, inclusivity, innovation, and excellence, fostering ethical conduct, social responsibility, and sustainability across all academic and administrative activities. Best practices at the institution include participative governance, transparent decision-making, continuous quality improvement in teaching and research, student-centric pedagogy, and mentoring systems that promote holistic development.

7.1 Innovation initiative

DJSCE is committed to fostering a culture of innovation, research, and intellectual growth through an enabling ecosystem for both faculty and students. The institute emphasizes continuous professional development by encouraging faculty members to participate in Faculty Development Programs, online certification courses, expert sessions, and webinars in emerging and conventional domains. It consistently promotes research and development initiatives, supports the creation of high-quality e-content, and provides enhanced e-library resources to strengthen academic and research engagement. At the student level, the institute offers a comprehensive, multidisciplinary, and holistic engineering education that nurtures creativity, problem-solving, and professional competence. Project-based learning and innovation-driven activities are integrated to improve employability and cultivate research aptitude. Students are regularly motivated to participate in national and international competitive forums, and DJSCE has distinguished itself as a leader in prestigious competitions, including the Smart India Hackathon, consistently demonstrating its students' strong technical capabilities. Additionally,

the implementation of Personal Development Plans (PDP) and soft skills training under the NEP-2020 framework supports their overall growth.

7.2 Best Practices in Education:

DJSCE implements a comprehensive Project-Based Learning (PBL) and Multidisciplinary Integration Framework that strengthens students' technical competence, creativity, and holistic development. Through structured mini-projects, capstone projects, industry-driven problem statements, and cross-departmental collaborations, students gain hands-on experience in applying engineering concepts to real-world challenges. This practice enhances critical thinking, innovation, and employability while promoting teamwork and interdisciplinary exposure. In alignment with NEP-2020, the framework is complemented by Personal Development Plans (PDP), soft skills training, and participation in national and international competitions that demonstrate students' acquired skills. Additionally, a strong emphasis is placed on inculcating ethical, civic, and community-oriented values through the vibrant NSS chapter of DJSCE, which engages students in socially impactful initiatives. This holistic approach nurtures graduates who are technically adept, socially responsible, ethically grounded, and industry-ready.

DJSCE has established a robust **Faculty Research and Innovation Enrichment Ecosystem** aimed at fostering continuous professional growth and strengthening the institute's research culture. The practice includes regular engagement in Faculty Development Programs, online certification courses, domain-specific expert sessions, and collaborative research initiatives. The institute provides access to advanced laboratories, enhanced e-library facilities, and dedicated R&D cells to support high-quality research output. Faculty members are encouraged to pursue interdisciplinary projects, publish in reputed journals, innovate through patentable work, and create quality e-content. This ecosystem not only upgrades faculty skill sets in emerging technologies but also drives institutional excellence by promoting innovation, knowledge creation, and intellectual property development.

Members of External Peer Team:

1. **Dr. Deven Shah**
2. **Dr. G.T. Thampi**
3. **Dr. Sudhakar Mande**

